Date: 14/05/2010

Exam No: \_\_\_\_

## **GANPAT UNIVERSITY**

## B. TECH SEM- IV (BM&I) CBCS REGULAR EXAMINATION- APRIL-JUNE 2016 2BM401: ANALOG INTEGRATED ELECTRONICS

TIME: 3 HRS **TOTAL MARKS: 60** Instructions: (1) This Question paper has two sections. Attempt each section in separate answer book. (2) Figures on right indicate marks. (3) Conventional terms and notations are used. (4) Draw figures & circuits, write equations and assume data wherever necessary. SECTION: I 0.1 (10)Define Integrated Circuits (IC). Differentiate between Analog and Digital IC's. Write its scale a) 5 of integration. Derive following equation for closed loop inverting amplifier given below: b) 5 1)  $A_F = -\frac{R_F}{R_1}$  2)  $A_F = \frac{AK}{1 + AB}$ Calculate output voltage for gain 10 and input voltage 2v. OR Q.1 (10)Draw different configurations of Differential amplifier using transistor. What is single ended a) 5 and double ended operation? b) Explain integrator with neat circuit diagram and write equation for calculating frequency. 5 Draw the output waveform if input is sine and square wave. Q.2 (10)Describe how inverting configuration of OP-AMP can work as summing, scaling and a) 5 averaging amplifier. Write output voltage equations. Draw and explain circuit diagram of zero crossing detector. Draw input-output waveforms for b) 5 non-inverting comparator of Op-Amp if input is 4v and Vref is +2V. OR Q.2 (10)a) What is Switching Mode Power Supply (SMPS)? Describe its working principle. 5

Draw and explain circuit diagram of zero crossing detector. Draw input-output waveforms for non-inverting comparator of Op-Amp if input is 4v and Vref is +2V.

OR

Q.2

a) What is Switching Mode Power Supply (SMPS)? Describe its working principle.

b) Write note on voltage controlled Oscillator.

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Q.3

Q.3

a) Design first order High pass Butterworth filter at 1KHz cutoff frequency with pass band gain of 2. Plot its frequency response. Assume necessary data.

b) Draw differential amplifier with one Op-Amp. Derive equation of voltage gain.

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## SECTION: II

Q.4		(10)
a)	With the help of block diagram explain working of 555 timer as Monostable Multivibrator?	5
b)	Define: Filter. Give classification of filter. What are the advantages of active filter over	5
	passive filter? Write application of filter.	
	OR	
Q.4		(10)
a)	Construct ± 5 V fixed power supply using voltage regulated IC.	5
b)	Explain working of sample and hold circuit using MOSFET.	5
		(10)
Q.5		
a)	Explain pin diagram of 555 timer and state its applications.	5
b)	Describe working of Instrumentation amplifier with circuit diagram and equations. Calculate	5
	the output voltage if inputs to amplifier is 6mv and 4mv respectively. Keep gain of amplifier	
	100.	
	OR	
Q.5		(10)
a)	With the help of circuit diagram explain RC phase shift oscillator. Write equation to calculate	5
	frequency if R is $10K\Omega$ and C is $0.01\mu F$ .	
b)	Define duty cycle. Write note on switching voltage regulator.	5
0.6		(10)
Q.6	The proof of the branch of particles and market of the deposition of the second of the	
a)	Write shot note on: Square wave generator.	3
c)	Distinguish between signal generator, oscillator and function generator.	3
b)	Write note on V to I converter.	4
	END OF PAPER	