GANPAT UNIVERSITY

B.TECH SEM V (ELECTRICAL ENGINEERING) REGULAR EXAMINATION NOV- DEC - 2014 2EE501: FUNDAMENTAL OF MICROPROCESSORS

Max T	ime: 3	hrs. Max Marks	s: 70				
Instru	ctions:		V				
1.	All questions are compulsory.						
2.	Answers to two sections must be written in separate answer books.						
3.	Figures to the right indicate full marks.						
4.	经国际分积损益 计设定性 医乳腺性 医乳腺性 医乳腺性 医乳腺性 医乳腺性 医乳腺性 医乳腺性 医乳腺						
		SECTION - I					
Q:1	(A)	Define instruction cycle. Also discuss instruction fetch and execute	(07)				
V.,	cycle with necessary timing diagram.						
	(B)	Explain following instructions.	(03)				
		(i) DAA (ii) DAD B					
	(C)	What is the purpose of ALE and HLDA pin in 8085 micro-processor?	(02)				
		OR					
Q:1	(A)	Sketch the architectural block diagram of 8085 microprocessor. Also	(06)				
	state the function of each block in brief.						
	(B)	Discuss different addressing modes available in 8085 microprocessor	(04)				
		with example.	(03)				
	(C)	Point out Valid and Invalid instructions: Correct invalid ones	(02)				
		(i) MVI AB (ii) ANI 0B h					
		(iii) LDA BD (iv) STA C000 h	(06)				
Q:2	(A)	Draw and explain the programming model of 8085 microprocessor.	(05)				
	(B)	(B) 10tal 10 data are stored at memory location starting notation					
		h. write a program to arrange all data in ascending order.					
		OR	(04)				
Q:2		What is program format? Illustrate with example.	(05)				
	(B) Define bus. Draw & explain ous structure of coos married						
	(C)	Specify the content of accumulator and status of flags when following	(02)				
		instructions are executed.					
		MVI A, B7 h					
0.3		ORA A	(12				
Q:3		How address/data lines AD0-AD7 are demultiplexed? Explain					
	(A)	Explain timing diagram of memory write cycle.					
	(B) (C)	Write a program to multiply two hex number which is stored at memory					
	(6)	location 2050 h and 2051 h, store result in memory location 2052 h.					
1		(Assume that result of multiplication doesn't generate any carry)					
ALIENSE .							

(i) higher & lower level languages

(ii) Call & Jump Instruction

(D) With suitable example, distinguish between

		SECTI	ON – II	Bernard and Control of the Control o					
Q:4	(A)	State and explain working of re	otate instruction	s of 8085 with proper	(06)				
V	(2.2)	avamnle .			12.0				
	(B)	What is PPI? Draw block diagra	also state the functions	(04)					
	Part Service	of each block.							
	(C)	20054 annumable interval timer							
100	agungs whose to be a more than the highest sign and the Arma Arma Arma Arma Arma Arma Arma Arma								
Q:4	(A)	Draw and explain the format of SIM and RIM instructions.							
	(B) Give the control word of 8255 and explain mode 1 input operation in								
	datail								
	(C)	(C) Write an ALP to find 2's compliment of a number stored in memory							
	location A000 Store result in B000.								
Q:5	(A)	Draw and explain the format of flag register for 8086 microprocessor.							
	(D) (i) Define: T = states Machine cycle								
	(ii) Write a subroutine program to generate a delay of 1 sec, when crystal								
	frequency of microprocessor is 3 Mhz.								
	OR								
Q:5	- (A) Write a program to provine the given on our unite to the								
	(Cross Vollow and Red) and two nedestrian sign (Walk & Doll t Walk).								
	The signal light and signs are turned on off by the data bits of output								
		port as shown below.	Data bits	On time					
		2.8	Data bits D ₀	20 Seconds					
		Green	D_0	5 Seconds					
		Yellow	D ₄	35 Seconds					
		Red	D_6	20 Seconds					
		Walk	D ₇	40 Seconds					
Don't Walk The traffic and pedestrian flow are in same direction: the pedestrian									
should cross the road when green light is on									
should closs the load when green groups									

(B) Using diagram illustrate pin out of 8086 microprocessor.

(06)(12)

Q:6 Attempt Any Two

- (A) List out major section of 8279 display controller also explain any one section in detail with suitable diagram.
- (B) What is stack and stack pointer? Explain the working and use of stack in subroutine program.
- Draw and explain logical block diagram of 8259 (PIC).

END OF PAPER